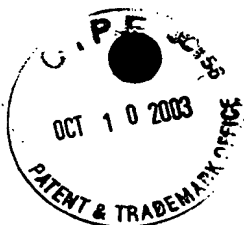


3408.65517



PATENT APPLICATION

2651

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re U.S. Application of: )  
)  
Applicant(s): Kazuhiuko Takaishi )  
)  
Serial No.: 09/802,188 )  
)  
Conf. No.: 5899 )  
)  
Filed: March 8, 2001 )  
)  
For: HEAD POSITIONING )  
CONTROL METHOD AND )  
DEVICE FOR STORAGE )  
DISK APPARATUS )  
)  
Art Unit: 2651 )  
)  
Examiner: Dan Davidson )

I hereby certify that this paper is being deposited with the United States Postal Service as FIRST-CLASS mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this date.

10/7/2003  
Date

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LETTER TO THE DRAFTSMAN

Commissioner for Patents  
P.O. Box 1450  
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Sir:

Please approve the changes shown in red on the attached marked-up  
copies of FIGS. 3, 4, 7, 8, and 10.

Respectfully submitted,

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FIG. 3

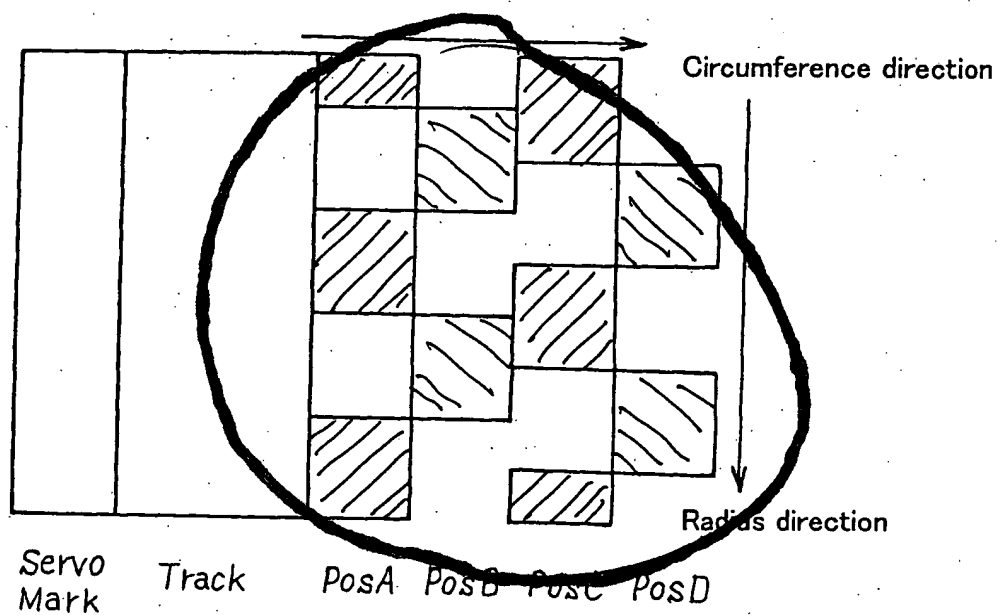
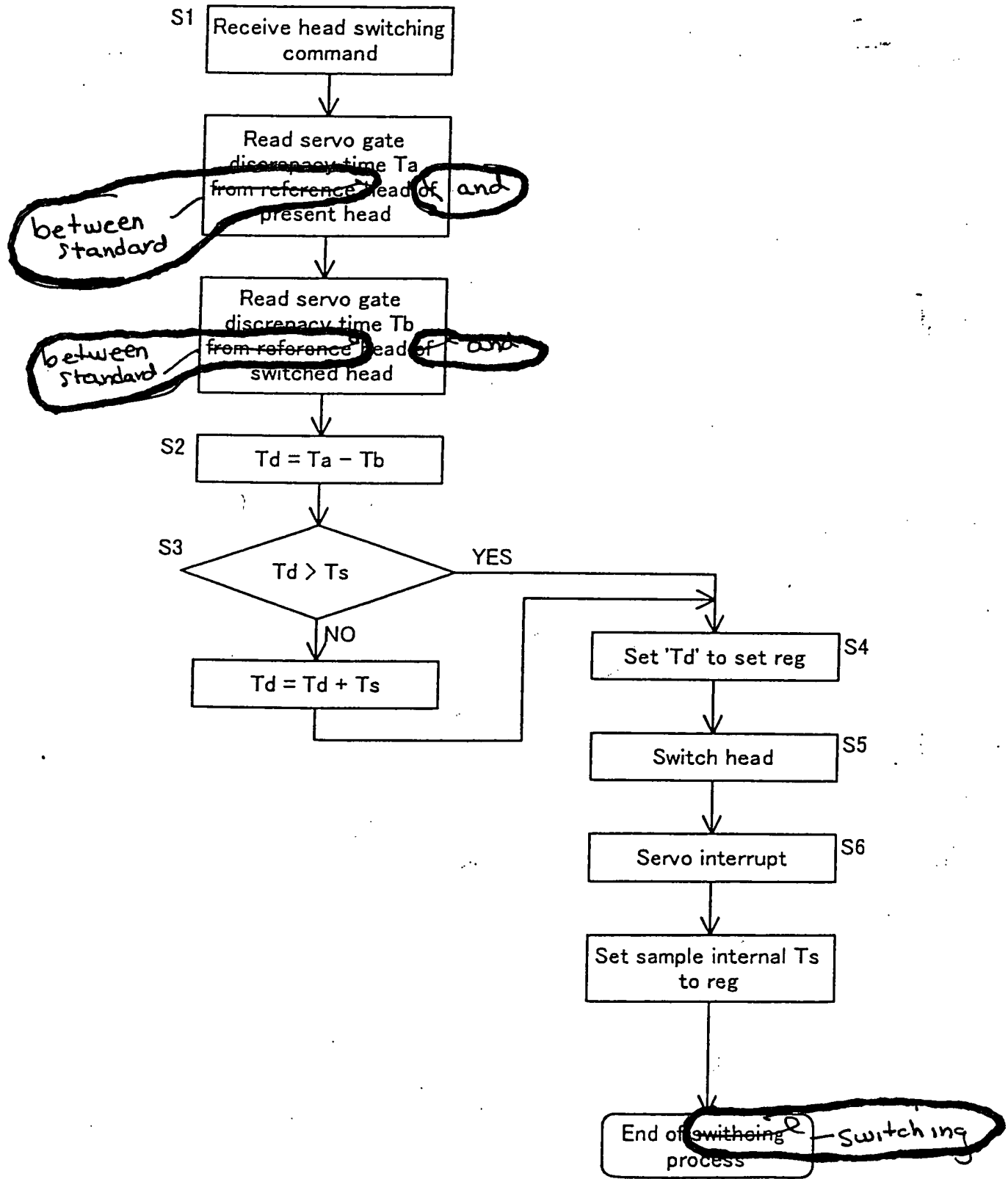




FIG. 4



The diagram illustrates a servo control system 6. It features a Head No. setting block 30, a REG block 31, a Comparator block 32, a Calculator block 33, a Selector block 34, a Sample interval memory block 35, a Gate time interval memory block 36, a Counter block 21, a Reference clock input, a Comparator block 22, a Gate generator block 23, a Head selector block 25, a Servo mark detector block 26, a Position signal detector block 27, a Position register block 28, and an MCU block 11. The system is designed to manage head selection and servo control based on a reference clock and various feedback signals.



FIG. 8

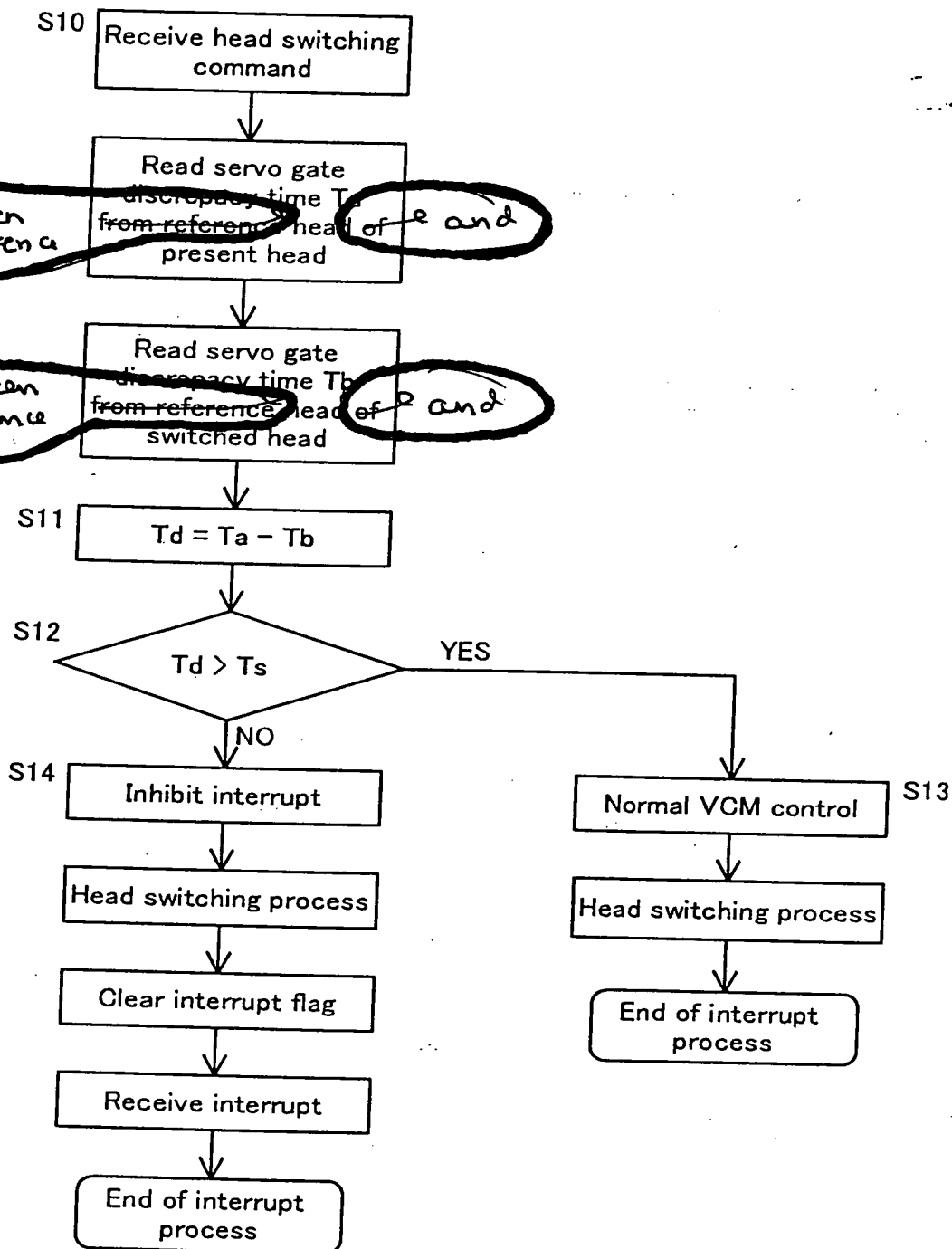




FIG. 10

